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	Application No.	Applicant(s)
Notice of Allowability	10/683,768	HAN, JONGHEE
	Examiner	Art Unit
	David Lam	2827
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this apport or other appropriate communication GHTS. This application is subject to	olication. If not included will be mailed in due course. THIS
1. This communication is responsive to		
2. The allowed claim(s) is/are <u>1-32</u> .		
3. The drawings filed on 10 October 2003 are accepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. Interview Summary Paper No./Mail Dat 7. Examiner's Amenda	e
		DAVID LAM PRIMARY EXAMEN

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Allowable Subject Matter

The following is an examiner's statement of reasons for allowance: Claims 1-32 are 1. allowable over the prior art of record because none of the prior art whether taken singularly or in combination, especially when these limitations are considered within the specific combination claimed, to teach: a circuit/control system for a DRAM device comprising: at least one buffer modules, among others as claimed in independent claims 9, 17, 21, each comprises a differential amplifiers having first input respond to a signal and second input responsive to a reference voltage, a common source stage, and an output stage, wherein the impedance of the common source stage is control by a source of a bias voltage, and the reference define the amplitude of the bias voltage; a circuit for an input buffer of a signal comprising a common source stage, among others as claimed in independent claim 25, connected between a common node and a ground reference of a differential amplifiers, wherein the impedance of the common source stage is control by a source of a bias voltage. Method of digitizing a signal comprising step of applying the reference voltage to define the amplitude of the bias voltage, and among other steps as claimed in independent claim 1; defining the amplitude of the bias voltage in response to receiving the reference voltage on the second input of the differential amplifier, and among other steps as claimed in independent claim 29.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- Sim (6,4424,577) discloses a sense amplifier circuit for use is a semiconductor memory device.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Lam whose telephone number is 571-272-1782. The examiner can normally be reached on 6:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai Ho can be reached on 571-272-1777. The fax phone numbers for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

D. Lam

DAVID LAM
PRIMARY EXAMINER

March 1, 2005